



6th Workshop on Compositional Theory and Technology for Real-Time Embedded Systems (CRTS)

Sessions: Salon C

Lunch: Tuscany Room

<p>9:00 – 10:30</p> <p>Keynote</p>	<p>Developing multi-periodic critical embedded systems on multi/many-core architectures <i>Claire Pagetti (ONERA, ENSEEIHT)</i></p>
<p>10:30 – 11:00</p>	<p>Break</p>
<p>11:00 – 12:30</p> <p>Session I: Scheduling and Resource Sharing</p>	<p>Budget allocations for hierarchical fixed-priority scheduling of sporadic tasks with deferred preemptions upon EDP resources <i>Martijn M.H.P. Van Den Heuvel, Reinder J. Bril and Johan Lukkien</i></p> <p>Logic-based Schedulability Analysis for Compositional Hard Real-Time Embedded Systems <i>André De Matos Pedro, David Pereira, Luís Miguel Pinho and Jorge Sousa Pinto</i></p> <p>Resource Sharing among Prioritized Real-Time Applications on Multiprocessors <i>Sara Afshar, Nima Moghaddami Khalilzad, Farhang Nemati and Thomas Nolte</i></p>
<p>12:30 – 14:00</p>	<p>Lunch</p>
<p>14:00 – 16:30</p> <p>Session II: Execution Time and Modeling</p>	<p>Towards Compositionality in Execution Time Analysis - Definition and Challenges <i>Sebastian Hahn, Jan Reineke and Reinhard Wilhelm</i></p> <p>Aligning Single Path Loops to Reduce the Number of Capacity Cache Misses <i>Bekim Cilku, Roland Kammerer and Peter Puschner</i></p> <p>Support for Cross-domain Composition of Embedded Systems Using MARTE Models <i>Maria Vasilevskaya and Simin Nadjm-Tehrani</i></p>
<p>16:30</p>	<p>Workshop Closing</p>