



1st Workshop on Real-Time Ethernet (RATE)

Sessions: Salon B

Lunch: Tuscany Room

8:30 – 8:45

Opening and welcome address

8:45 – 10:30

Session 1: RTE in industry

Deterministic Ethernet: Standardization in Progress and Beyond

*Wilfried Steiner, TTTech Computertechnik AG
Norman Finn, CISCO Systems*

Ethernet as Future Automotive Communication Backbone

Rolf Ernst, Philip Axer, Daniel Thiele, Technical University of Braunschweig, Germany

DCs vs time-triggered Ethernet

Joey Stubbs, EtherCAT Technology Group

10:30 – 11:00

Break

11:00 – 12:30

Session 2: RTE in avionics

Trends in Avionics switched Ethernet networks

Christian Fraboul, Jean-Luc Scharbarg, University of Toulouse, France

SYMADE: a Novel Avionics Data-Acquisition Network based on Daisy-chain Ethernet

Ahlem Mifdaoui, Higher Institute for Air and Space (ISAE), France

Modeling and schedulability analysis of AFDX networks in MAST 2

J. Javier Guti rrez, University of Cantabria, Spain

12:30 – 14:00

Lunch

14:00 – 15:45

Session 3: Alternative RTE protocols

Developments on Flexible Time-Triggered Switched Ethernet

*(1) Luis Almeida, Zahid Iqbal,
(2) Paulo Pedreiras, Ricardo Marau, Luis Silva,
(3) Mohammad Ashjaei, Moris Behnam, Thomas Nolte,
(4) Julian Proenza, Manuel Barranco, David Gessner, Guillermo Rodriguez-Navas, Alberto Balasteros, Sinisa Derasevic
1- University of Porto, Portugal; 2- University of Aveiro, Portugal
3- Malardalen University, Sweden; 4- University of the Balearic Islands, Spain*

Atacama: An Open Experimental Platform for Mixed-Criticality Networking on Top of Ethernet

*(1) Gonzalo Carvajal, (2) Sebastian Fischmeister
1- University of Concepci n, Chile; 2- University of Waterloo, Canada*

Introducing support for time-sensitive traffic over Ethernet switches

L. Lo Bello, G. Alderisi, G. Patti, University of Catania, Italy

Real-Time Ethernet and PCI Express: Do they fit together?

*Michal Sojka, Rostislav Lisovy, Zdenek Hanzalek
Czech Technical University*

15:45

Workshop Closing